Refine Search

Search Results -

Terms	Documents
(370/254 |370/419 |709/220 |709/221 |709/250 |709/226 |710/8 |710/9 |710/10 |710/33 |710/315 |710/63 |710/305 |710/100 |712/36 |714/23.711).ccls.	6316

Database:
- US Pre-Grant Publication Full-Text Database
- **US Patents Full-Text Database**
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: L1

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Friday, February 13, 2004 Printable Copy Create Case

Set Name side by side	Query
	DB=USPT; PLUR=YES; OP=OR
L1	710/8-10,33,315,63,305,100;714/23.711/100,104,102,147;712/36;709/220,221,250,226;370/254,419.ccl

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L4	26

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L5

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Friday, February 13, 2004 Printable Copy Create Case

Set Name side by side	Query
	DB=USPT; PLUR=YES; OP=OR
L5	l1 and L4
L4	L3 same (area or location or address)
L3	L2 same host
L2	("universal seral bus" or USB) same memory
L1	710/8-10,33,315,63,305,100;714/23.711/100,104,102,147;712/36;709/220,221,250,226;370/254,419.ccl

END OF SEARCH HISTORY

h e b b cg b e e ch

Refine Search

Search Results -

Terms	Documents
L7 and (vendor or manufacturer)	10

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L8

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Friday, February 13, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L8	L7 and (vendor or manufacturer)	10	L8
L7	6012103.uref.	20	L7
L6	5850021.uref.	0	L6
L5	L1 and (vendor or manufacturer)	2	L5
L4	L1 and (non adj1 volatile)	1	L4
L3	L1 and nonvolatile	0	L3
L2	L1 and (memory same data same control$4)	1	L2
L1	6219736.pn. or 5850021.pn. or 6012103.pn.	3	L1

END OF SEARCH HISTORY

h e b b cg b e e ch

Refine Search

Search Results -

Terms	Documents
L7 and (vendor or manufacturer)	0

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L9

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Friday, February 13, 2004 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L9	L7 and (vendor or manufacturer)	0	L9
DB=USPT; PLUR=YES; OP=OR			
L8	L7 and (vendor or manufacturer)	10	L8
L7	6012103.uref.	20	L7
L6	5850021.uref.	0	L6
L5	L1 and (vendor or manufacturer)	2	L5
L4	L1 and (non adj1 volatile)	1	L4
L3	L1 and nonvolatile	0	L3
L2	L1 and (memory same data same control$4)	1	L2
L1	6219736.pn. or 5850021.pn. or 6012103.pn.	3	L1

END OF SEARCH HISTORY

h e b b cg b e e ch

Refine Search

Search Results -

Terms	Documents
"universal serial bus" same "power-on reset"	5

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L1

Refine Search | Recall Text | Clear | Interrupt

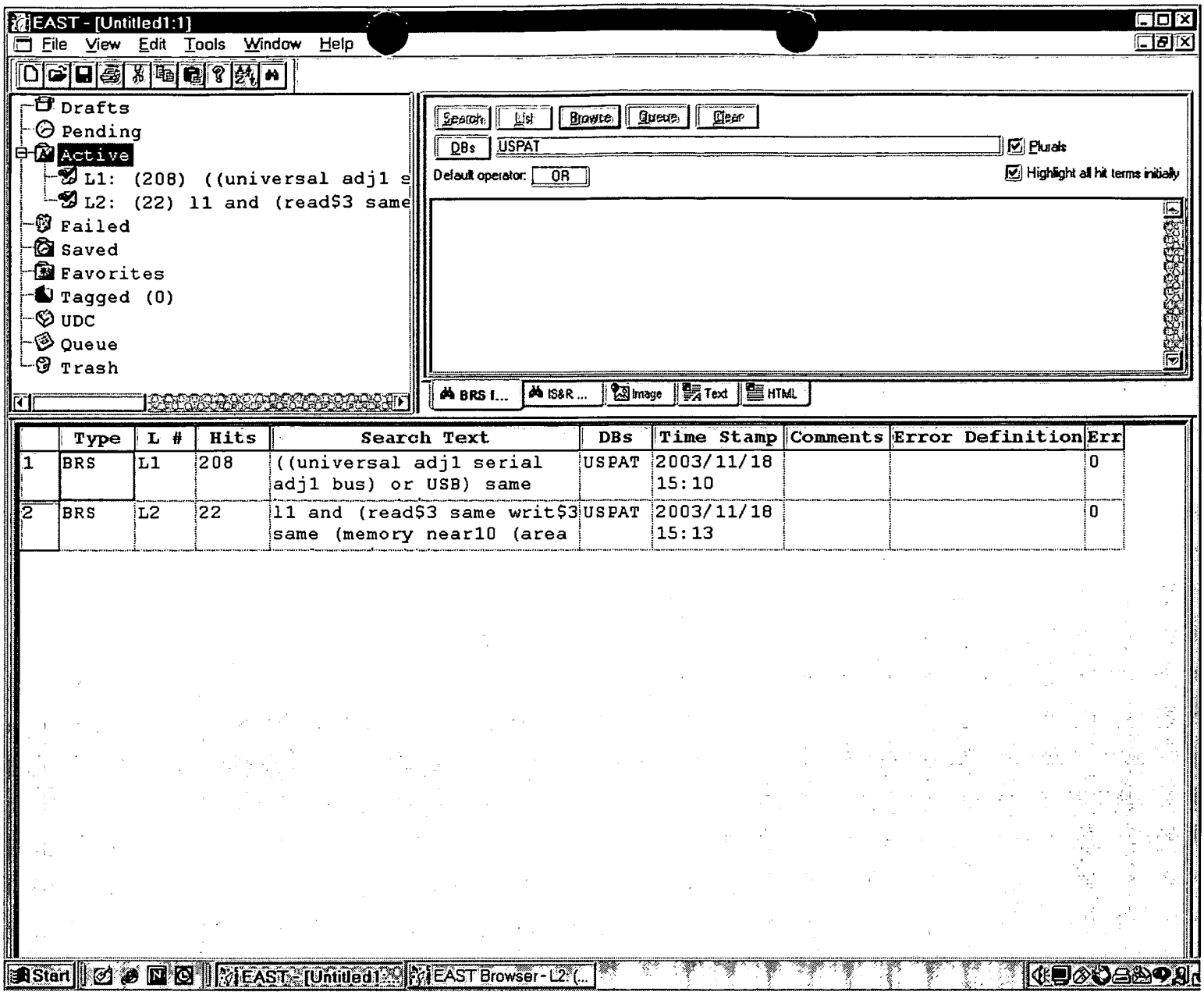
Search History

DATE: Friday, February 13, 2004 Printable Copy Create Case

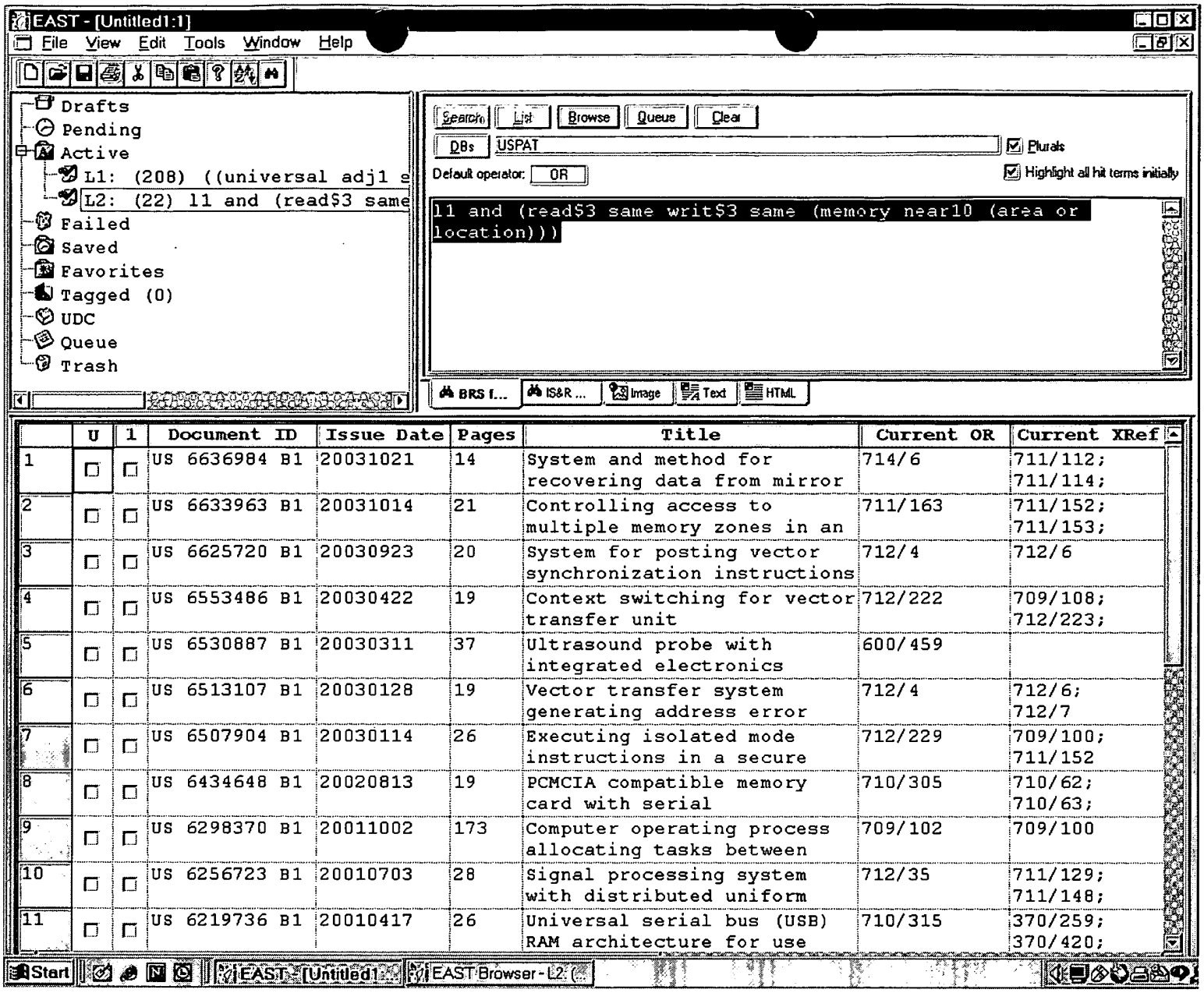
Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=OR			
L1	"universal serial bus" same "power-on reset"	5	L1

END OF SEARCH HISTORY

h e b b cg b e e ch



	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	208	((universal adj1 serial adj1 bus) or USB) same	USPAT	2003/11/18 15:10			0
2	BRS	L2	22	l1 and (read$3 same writ$3 same (memory near10 (area	USPAT	2003/11/18 15:13			0



	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	☐	☐	US 6636984 B1	20031021	14	System and method for recovering data from mirror	714/6	711/112; 711/114;
2	☐	☐	US 6633963 B1	20031014	21	Controlling access to multiple memory zones in an	711/163	711/152; 711/153;
3	☐	☐	US 6625720 B1	20030923	20	System for posting vector synchronization instructions	712/4	712/6
4	☐	☐	US 6553486 B1	20030422	19	Context switching for vector transfer unit	712/222	709/108; 712/223;
5	☐	☐	US 6530887 B1	20030311	37	Ultrasound probe with integrated electronics	600/459	
6	☐	☐	US 6513107 B1	20030128	19	Vector transfer system generating address error	712/4	712/6; 712/7
7	☐	☐	US 6507904 B1	20030114	26	Executing isolated mode instructions in a secure	712/229	709/100; 711/152
8	☐	☐	US 6434648 B1	20020813	19	PCMCIA compatible memory card with serial	710/305	710/62; 710/63;
9	☐	☐	US 6298370 B1	20011002	173	Computer operating process allocating tasks between	709/102	709/100
10	☐	☐	US 6256723 B1	20010703	28	Signal processing system with distributed uniform	712/35	711/129; 711/148;
11	☐	☐	US 6219736 B1	20010417	26	Universal serial bus (USB) RAM architecture for use	710/315	370/259; 370/420;

Your search matched **[0]** of **[987057]** documents.

You may refine your search by editing the current search expression or entering a new one the text box. Then click search Again.

(((universal serial bus) or usb)and (host and (microcomputer or computer))) and(read* ar

Search Again

OR

Use your browser's back button to return to your original search page.

Results:

No documents matched your query.

Your search matched **3** of **985183** documents.

A maximum of **3** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text b
Then click **Search Again.**

(((universal serial bus) or usb)and (host and (microcomputer or computer)))

Search Again

Results:
Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**

1 **Universal serial bus enhances virtual instrument based distributed pc monitoring**
Chung-Ping Young; Devaney, M.J.;
Instrumentation and Measurement Technology Conference, 2000. IMTC 2000. Proceedings of the 17th IEEE , Volume: 2 , 1-4 May 2000
Page(s): 920 -924 vol.2

[Abstract] [PDF Full-Text (496 KB)] **IEEE CNF**

2 **Universal serial bus enhances virtual instrument-based distributed pc monitoring**
Chung-Ping Young; Devaney, M.J.; Shyh-Chyang Wang;
Instrumentation and Measurement, IEEE Transactions on , Volume: 50 Issue: 6 2001
Page(s): 1692 -1697

[Abstract] [PDF Full-Text (190 KB)] **IEEE JNL**

3 **A very low-cost portable multichannel analyzer**
Cardoso, J.M.; Amorim, V.; Bastos, R.; Madeira, R.; Basilio Simoes, J.; Correia, C.M.B.A.;
Nuclear Science Symposium Conference Record, 2000 IEEE , Volume: 2 , 15-20 2000
Page(s): 12/164 -12/167 vol.2

[Abstract] [PDF Full-Text (352 KB)] **IEEE CNF**

Universal serial bus enhances virtual instrument based distributed power monitoring

Chung-Ping Young Devaney, M.J.
Digital Power Instrum. Group, Missouri Univ., Columbia, MO;

This paper appears in: **Instrumentation and Measurement Technology Conference, 2000. IMTC 2000. Proceedings of the 17th IEEE**
Meeting Date: 05/01/2000 -05/04/2000
Publication Date: 2000
Location: Baltimore, MD , USA
On page(s): 920-924 vol.2
Volume: 2, References Cited: 11
Number of Pages: 3 vol. (xliv+1615)
INSPEC Accession Number: 6678077

Abstract:
The Universal Serial Bus (USB) links a distributed multi-circuit power monitor system to its virtual instrument (VI) host computer. The distributed interconnection provides enhanced system performance at reduced cost, sinc USB host controller is a standard function of up-to-date personal computers (and low-cost high-performance USB hardware is readily available. The 12 Mb recently updated to 480 Mbps, transfer rate of USB improves data transmissi capability and boosts the real-time performance of the whole VI monitoring s when compared with conventional serial communications. In addition, the US dynamic attachment and removal feature allows each meter to be connected disconnected from its VI host computer without any system interruption. How the complete VI solutions are not currently supported by any USB hosts and devices. This paper presents an innovative USB VI strategy to implement integrated power monitoring applications and shows its superior performance

Index Terms:
computerised monitoring peripheral interfaces power engineering computing power s measurement virtual instrumentation 12 Mbit/s 480 Mbit/s USB host controller data transmission capability distributed interconnection distributed multi-circuit power monit system dynamic attachment and removal feature enhanced system performance low high-performance hardware real-time performance reduced cost service configuratio software architecture universal serial bus virtual instrument based system

D cuments that cite this d cument
Select link to view other documents in the database that cite this one.

Your search matched **1** of **1003743** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or enteri new one in the text box.

(universal serial bus)<and>(vendor or manufacurer) Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

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1 **FireWire finally comes home**
Severance, C.;
Computer , Volume: 31 , Issue: 11 , Nov. 1998
Pages:117 - 118

[Abstract] [PDF Full-Text (168 KB)] IEEE JNL

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h eee e eee g e ch e ch e e e e b c

Your search matched **3** of **1003743** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

(universal serial bus) and data and control Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

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1 **New protocol transfer module for USB2.0-to-IEEE 1394 interfaces using synchronous packet control for audio and video data streams**
Fujimori, S.; Sakurai, T.; Ikenoya, K.; Shinmura, S.; Asada, H.;
Consumer Electronics, 2002. ICCE. 2002 Digest of Technical Papers. International Conference on , 18-20 June 2002
Pages:314 - 315

[Abstract] [PDF Full-Text (292 KB)] **IEEE CNF**

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2 **A robust, load-insensitive pad driver**
Dowlatabadi, A.B.;
Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 4 , April 2000
Pages:660 - 665

[Abstract] [PDF Full-Text (224 KB)] **IEEE JNL**

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3 **A single chip terminal s luti n f r high-end teleph ne applicati ns**
Nair, V.; Erdmann, M.; Mishra, S.; Povazanec, J.; Shaligram, A.; Hu Chun Feng;
Custom Integrated Circuits, 2001, IEEE Conference on. , 6-9 May 2001
Pages:417 - 420

[Abstract] [PDF Full-Text (628 KB)] **IEEE CNF**

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Hit List

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 1 through 10 of 26 returned.

☐ 1. Document ID: US 6615306 B1

L5: Entry 1 of 26 File: USPT Sep 2, 2003

US-PAT-NO: 6615306
DOCUMENT-IDENTIFIER: US 6615306 B1

TITLE: Method and apparatus for reducing flow control and minimizing interface acquisition latency in a hub interface

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 2. Document ID: US 6523081 B1

L5: Entry 2 of 26 File: USPT Feb 18, 2003

US-PAT-NO: 6523081
DOCUMENT-IDENTIFIER: US 6523081 B1

TITLE: Architecture using dedicated endpoints and protocol for creating a multi-application interface and improving bandwidth over universal serial bus

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 3. Document ID: US 6493770 B1

L5: Entry 3 of 26 File: USPT Dec 10, 2002

US-PAT-NO: 6493770
DOCUMENT-IDENTIFIER: US 6493770 B1

TITLE: System for reconfiguring a peripheral device by downloading information from a host and electronically simulating a physical disconnection and reconnection to reconfigure the device

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 4. Document ID: US 6389495 B1

L5: Entry 4 of 26 File: USPT May 14, 2002

h e b b g e e e f e c g ef b e

US-PAT-NO: 6389495
DOCUMENT-IDENTIFIER: US 6389495 B1

TITLE: Dedicated circuit and method for enumerating and operating a peripheral device on a universal serial bus

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 5. Document ID: US 6381666 B1

L5: Entry 5 of 26 File: USPT Apr 30, 2002

US-PAT-NO: 6381666
DOCUMENT-IDENTIFIER: US 6381666 B1
** See image for Certificate of Correction **

TITLE: Method and apparatus for extending the range of the universal serial bus protocol

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 6. Document ID: US 6349354 B1

L5: Entry 6 of 26 File: USPT Feb 19, 2002

US-PAT-NO: 6349354
DOCUMENT-IDENTIFIER: US 6349354 B1
** See image for Certificate of Correction **

TITLE: Method to reduce system bus load due to USB bandwidth reclamation

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 7. Document ID: US 6292859 B1

L5: Entry 7 of 26 File: USPT Sep 18, 2001

US-PAT-NO: 6292859
DOCUMENT-IDENTIFIER: US 6292859 B1

TITLE: Automatic selection of an upgrade controller in an expansion slot of a computer system motherboard having an existing on-board controller

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 8. Document ID: US 6249825 B1

L5: Entry 8 of 26 File: USPT Jun 19, 2001

US-PAT-NO: 6249825

h e b b g e e e f e c g ef b e

DOCUMENT-IDENTIFIER: US 6249825 B1

TITLE: Universal serial bus interface system and method

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 9. Document ID: US 6226701 B1

L5: Entry 9 of 26 File: USPT May 1, 2001

US-PAT-NO: 6226701
DOCUMENT-IDENTIFIER: US 6226701 B1
**** See image for Certificate of Correction ****

TITLE: Method and system for accurate temporal determination of real-time events within a universal serial bus system

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 10. Document ID: US 6219736 B1

L5: Entry 10 of 26 File: USPT Apr 17, 2001

US-PAT-NO: 6219736
DOCUMENT-IDENTIFIER: US 6219736 B1

TITLE: Universal serial bus (USB) RAM architecture for use with microcomputers via an interface optimized for integrated services device network (ISDN)

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
L1 and L4	26

Display Format: TI Change Format

Previous Page Next Page Go to Doc#

h e b b g e e e f e c g ef b e

Hit List

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 11 through 20 of 26 returned.

☐ 11. Document ID: US 6216183 B1

L5: Entry 11 of 26 File: USPT Apr 10, 2001

US-PAT-NO: 6216183
DOCUMENT-IDENTIFIER: US 6216183 B1
** See image for Certificate of Correction **

TITLE: Apparatus and method for securing information entered upon an input device coupled to a universal serial bus

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 12. Document ID: US 6205501 B1

L5: Entry 12 of 26 File: USPT Mar 20, 2001

US-PAT-NO: 6205501
DOCUMENT-IDENTIFIER: US 6205501 B1

TITLE: Apparatus and method for handling universal serial bus control transfers

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 13. Document ID: US 6157975 A

L5: Entry 13 of 26 File: USPT Dec 5, 2000

US-PAT-NO: 6157975
DOCUMENT-IDENTIFIER: US 6157975 A

TITLE: Apparatus and method for providing an interface to a compound Universal Serial Bus controller

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 14. Document ID: US 6128669 A

L5: Entry 14 of 26 File: USPT Oct 3, 2000

h e b b cg b cc e

US-PAT-NO: 6128669
DOCUMENT-IDENTIFIER: US 6128669 A

TITLE: System having a bridge with distributed burst engine to decouple input/output task from a processor

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 15. Document ID: US 6122676 A

L5: Entry 15 of 26 File: USPT Sep 19, 2000

US-PAT-NO: 6122676
DOCUMENT-IDENTIFIER: US 6122676 A

TITLE: Apparatus and method for transmitting and receiving data into and out of a universal serial bus device

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 16. Document ID: US 6119190 A

L5: Entry 16 of 26 File: USPT Sep 12, 2000

US-PAT-NO: 6119190
DOCUMENT-IDENTIFIER: US 6119190 A

TITLE: Method to reduce system bus load due to USB bandwidth reclamation

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

---

☐ 17. Document ID: US 6085265 A

L5: Entry 17 of 26 File: USPT Jul 4, 2000

US-PAT-NO: 6085265
DOCUMENT-IDENTIFIER: US 6085265 A

TITLE: System for handling an asynchronous interrupt a universal serial bus device

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 18. Document ID: US 6073205 A

L5: Entry 18 of 26 File: USPT Jun 6, 2000

US-PAT-NO: 6073205
DOCUMENT-IDENTIFIER: US 6073205 A

TITLE: System and method of write posting in a universal serial bus system

h e b b cg b cc e

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 19. Document ID: US 6073193 A

L5: Entry 19 of 26 File: USPT Jun 6, 2000

US-PAT-NO: 6073193
DOCUMENT-IDENTIFIER: US 6073193 A

TITLE: Fail safe method and apparatus for a USB device

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 20. Document ID: US 6070208 A

L5: Entry 20 of 26 File: USPT May 30, 2000

US-PAT-NO: 6070208
DOCUMENT-IDENTIFIER: US 6070208 A

TITLE: Apparatus and method for implementing a versatile USB endpoint pipe

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
L1 and L4	26

Display Format: TI Change Format

Previous Page Next Page Go to Doc#

h e b b cg b cc e